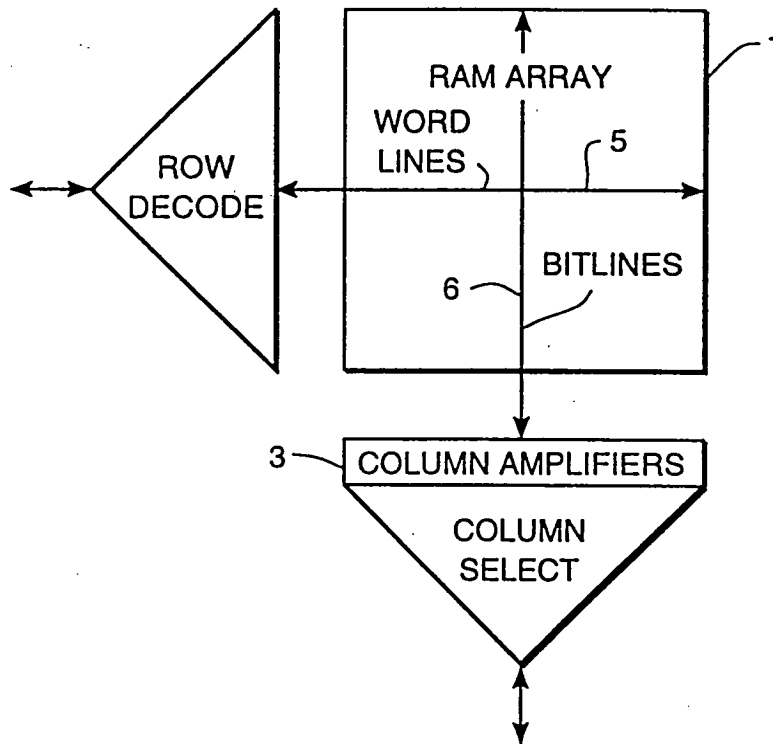


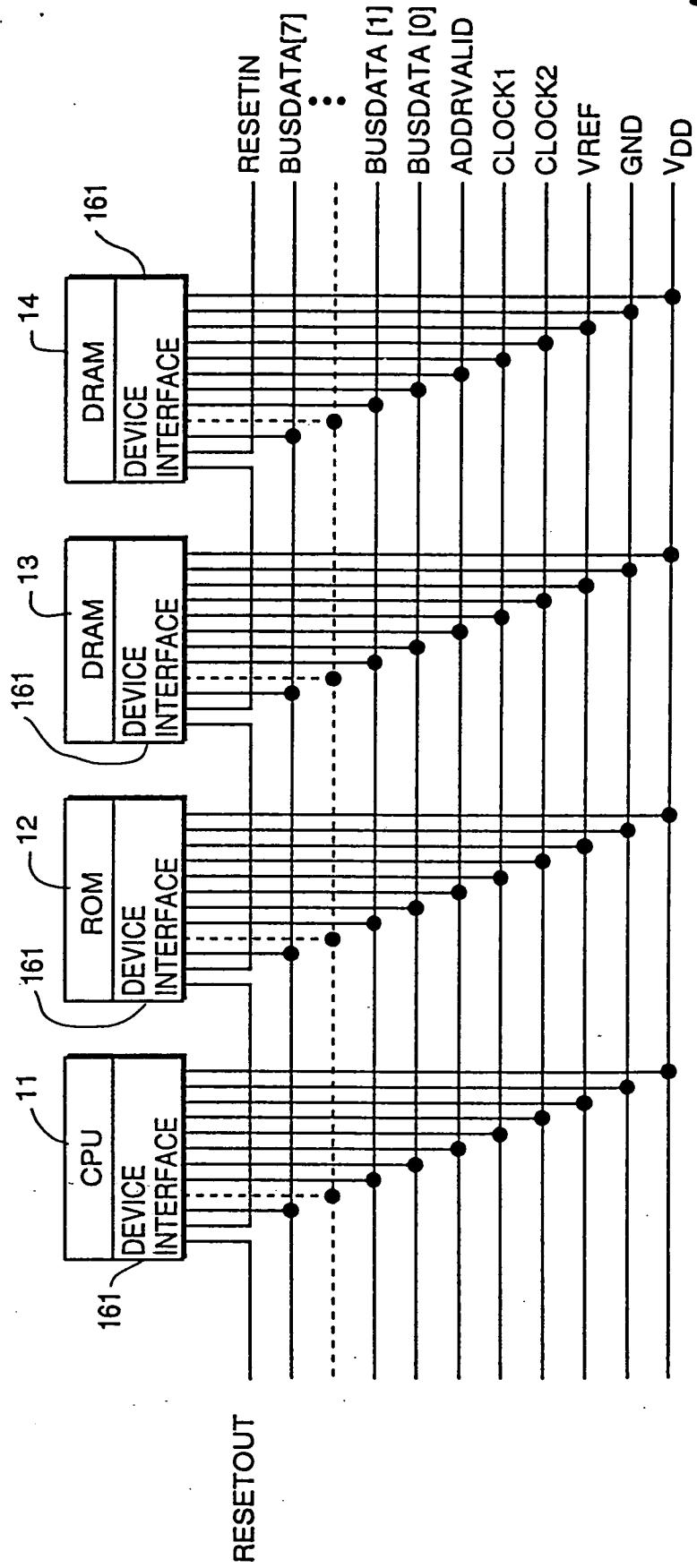
6260097

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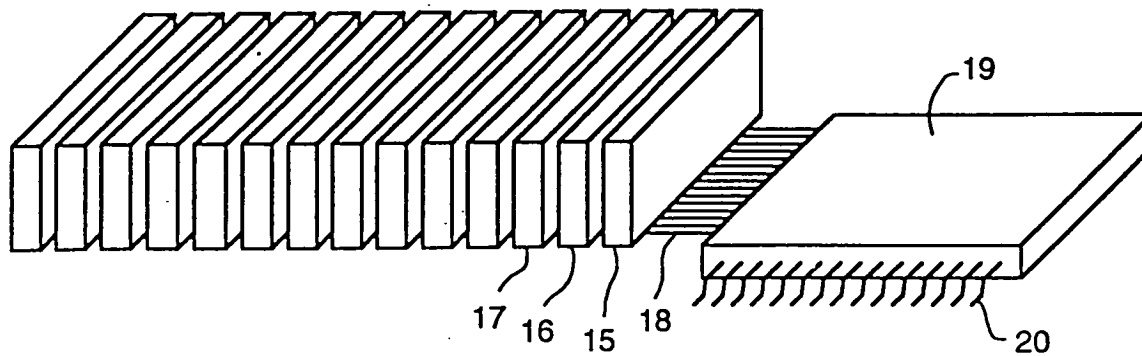
**FIG\_1**



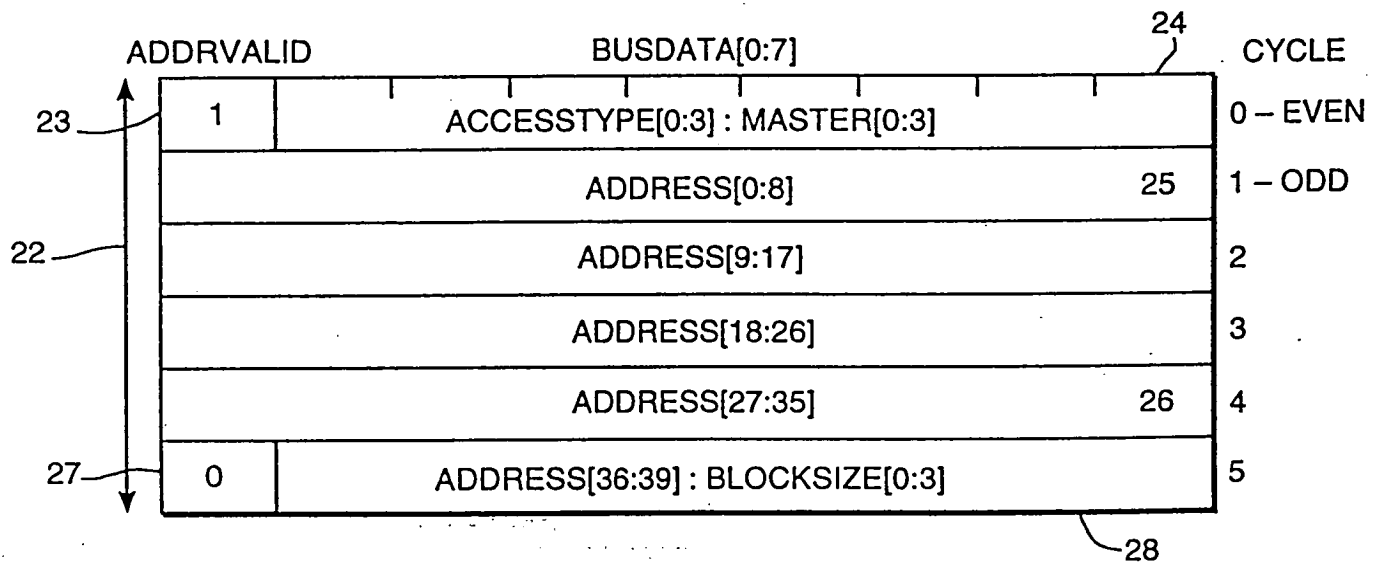
**FIG. 2**



# FIG 3

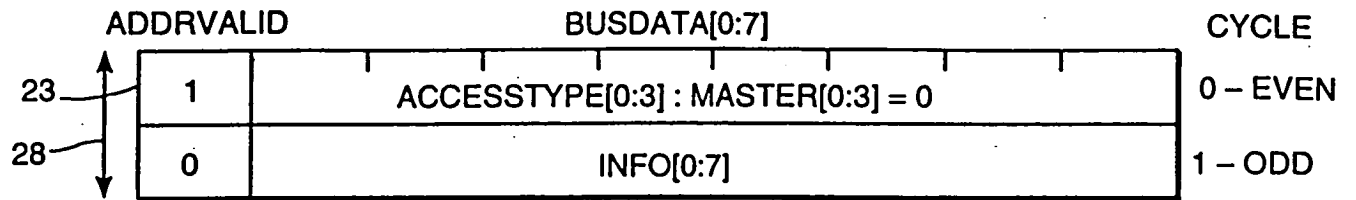


## REGULAR ACCESS

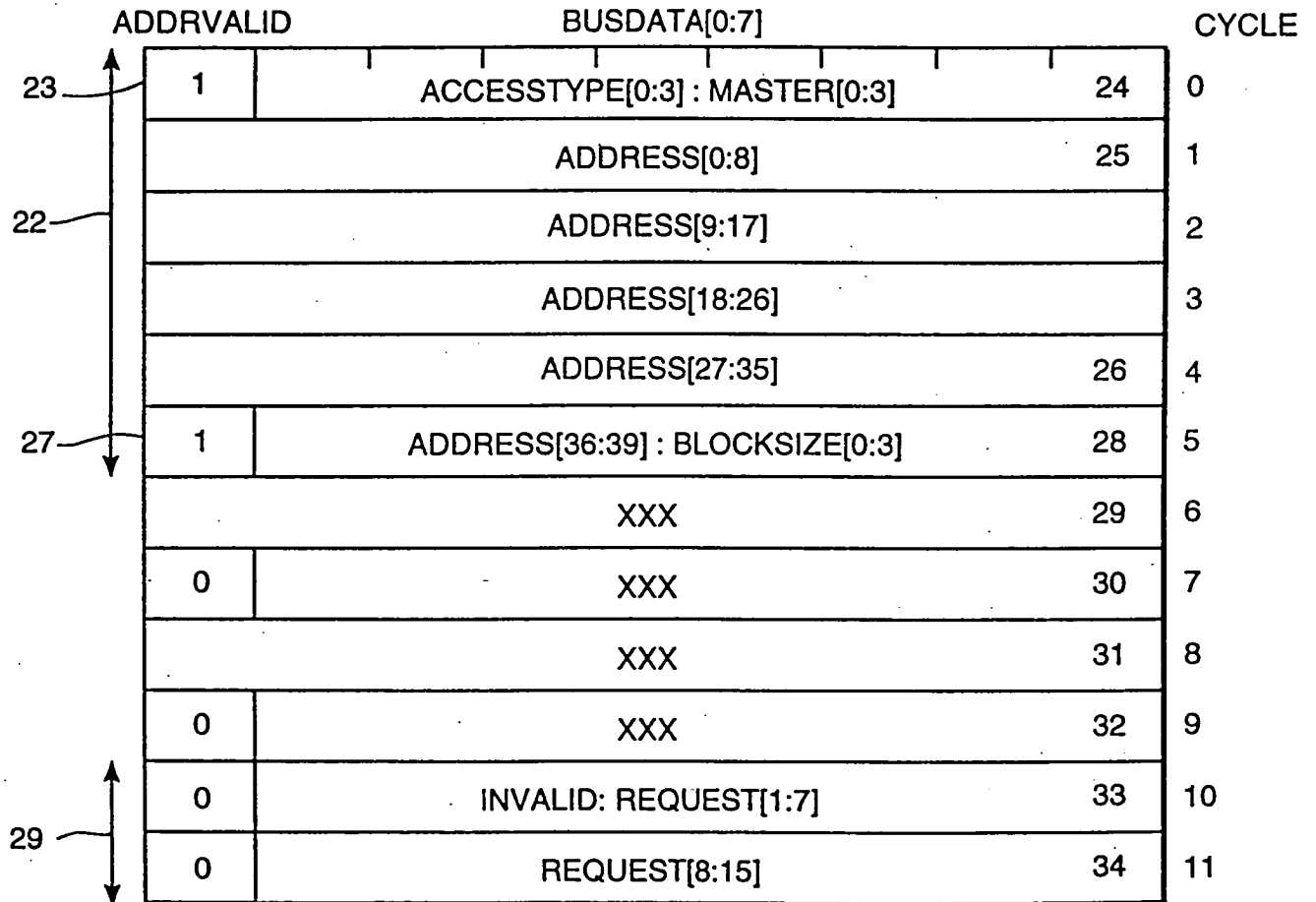


# FIG 4

# REJECT (NACK) CONTROL PACKET

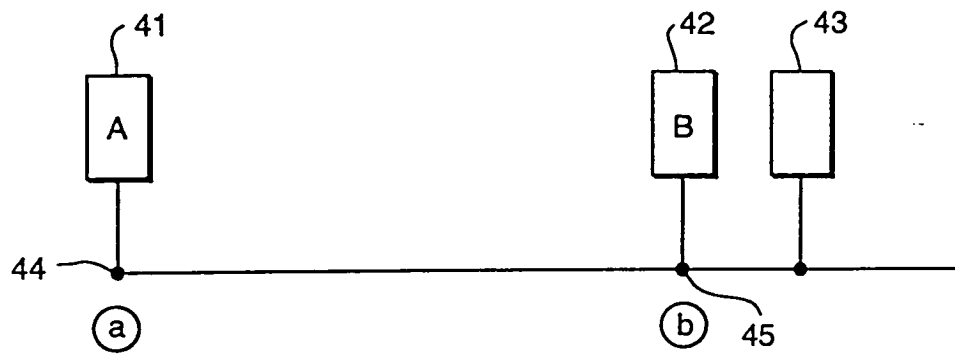


**FIG 5**



**FIG 6**

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**FIG 7A**

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VOLTAGE LOGICAL  
VALUE

2 0

V(a)  
AT 44

1.5

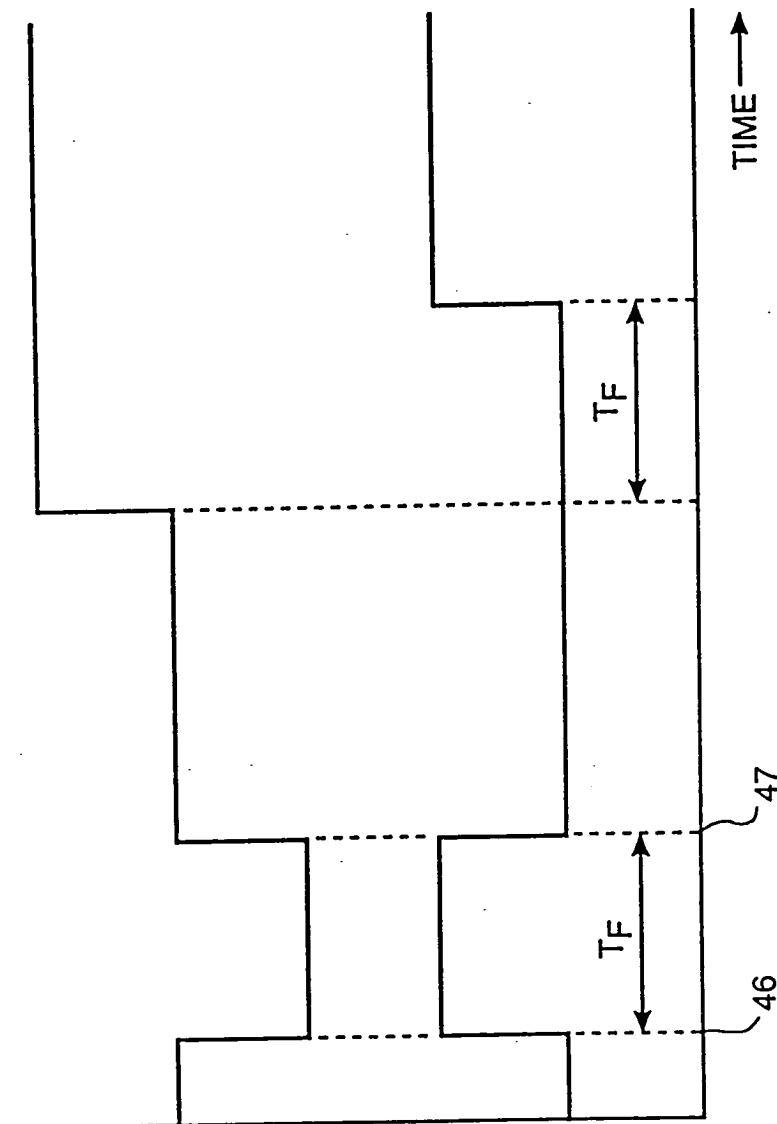
1

>1

0

V(b)  
AT 45

1

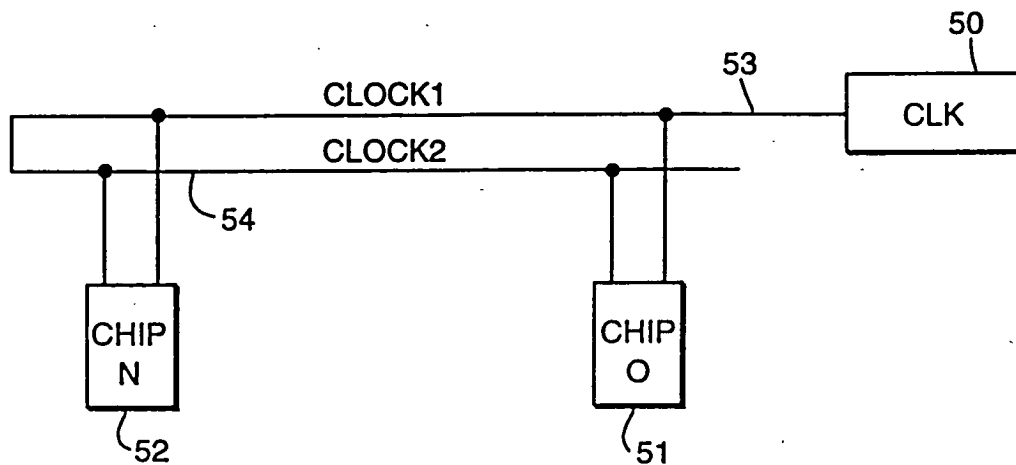


A OFF

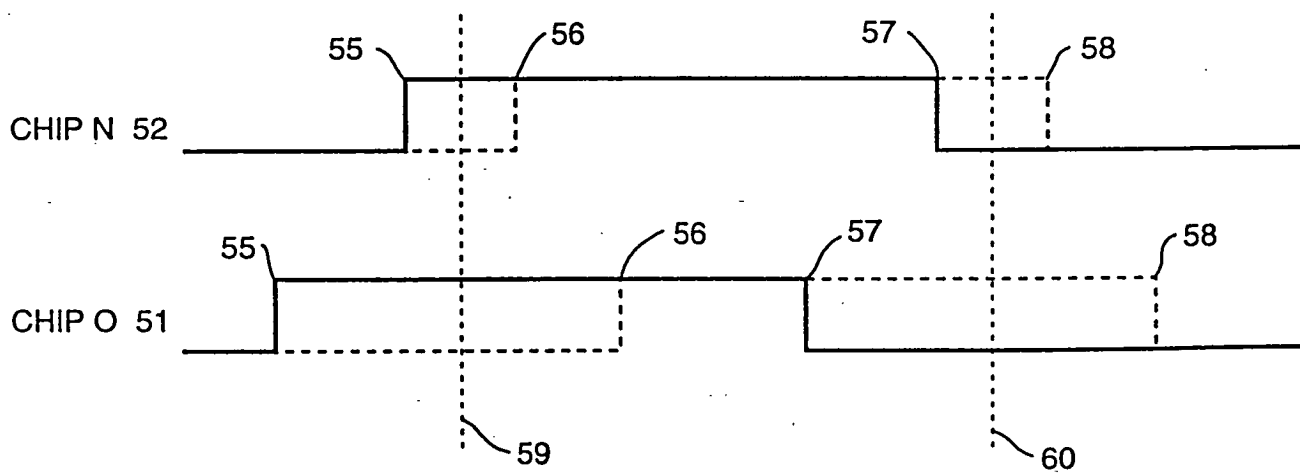
A ON  
BOFF

FILE 7B

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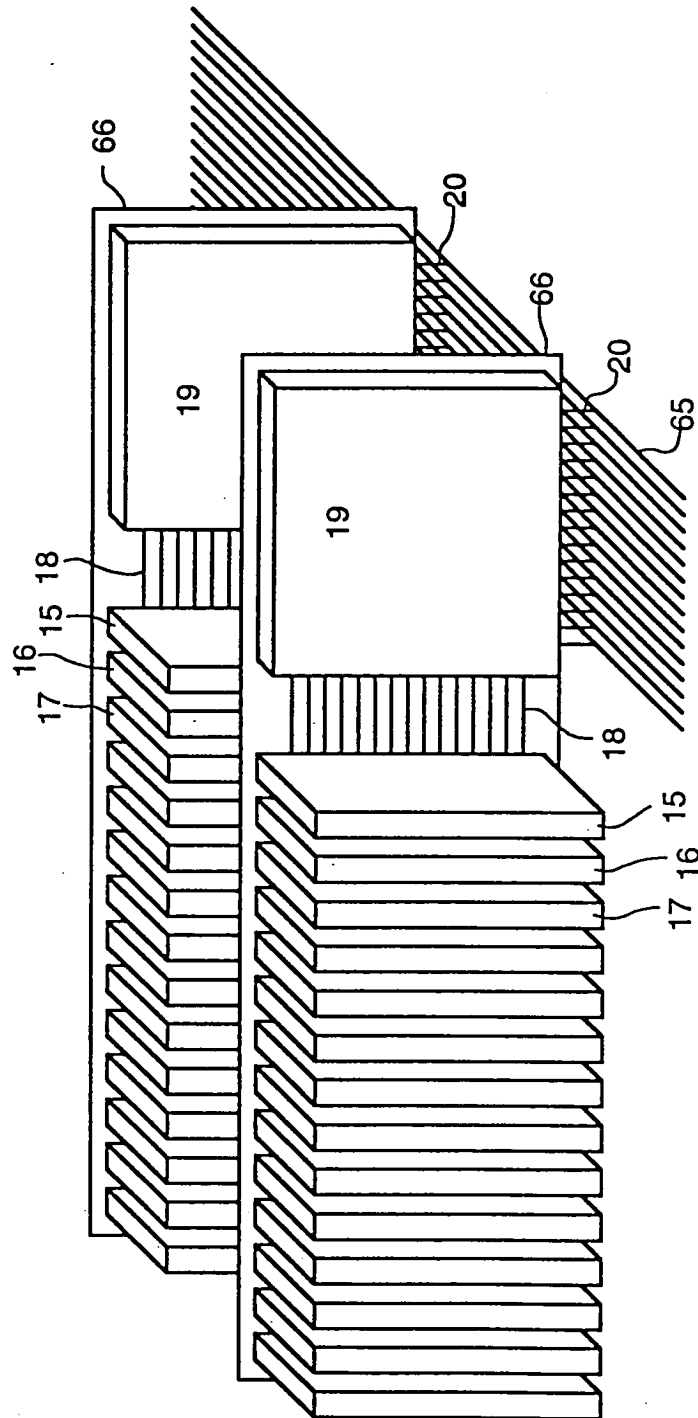
**FIG. 8A**



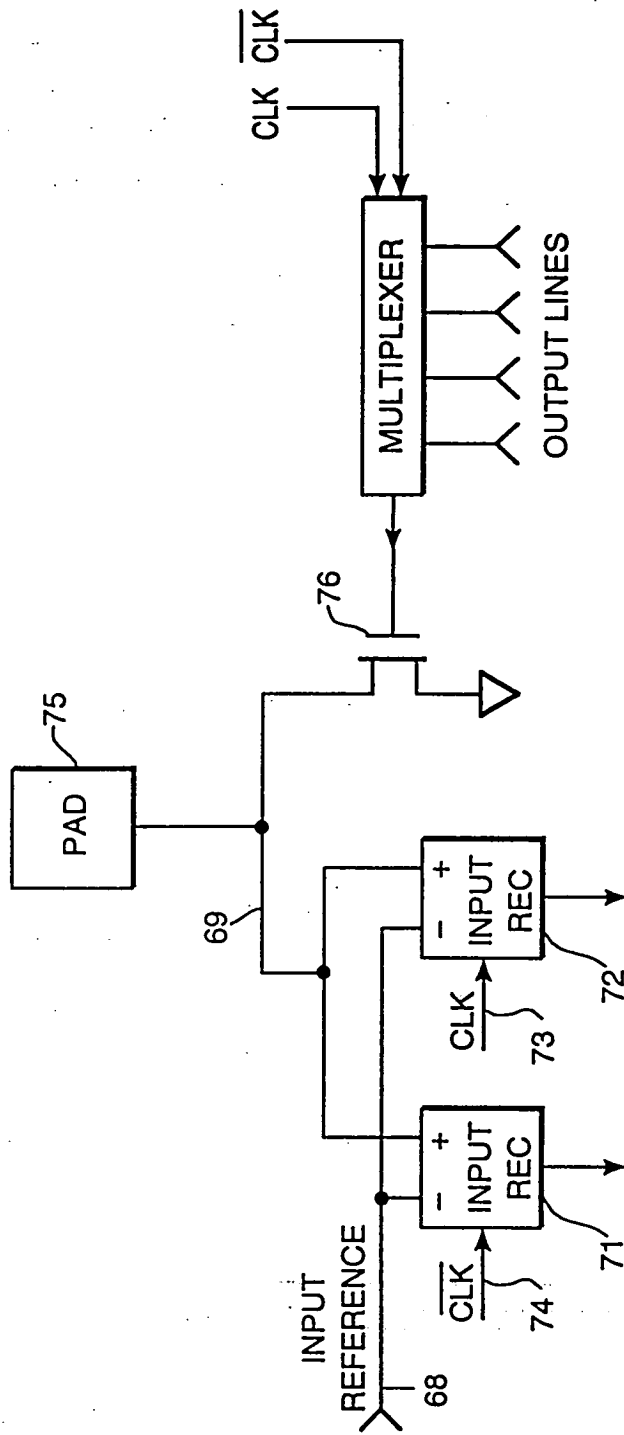
**FIG. 8B**

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**FIG. 9**

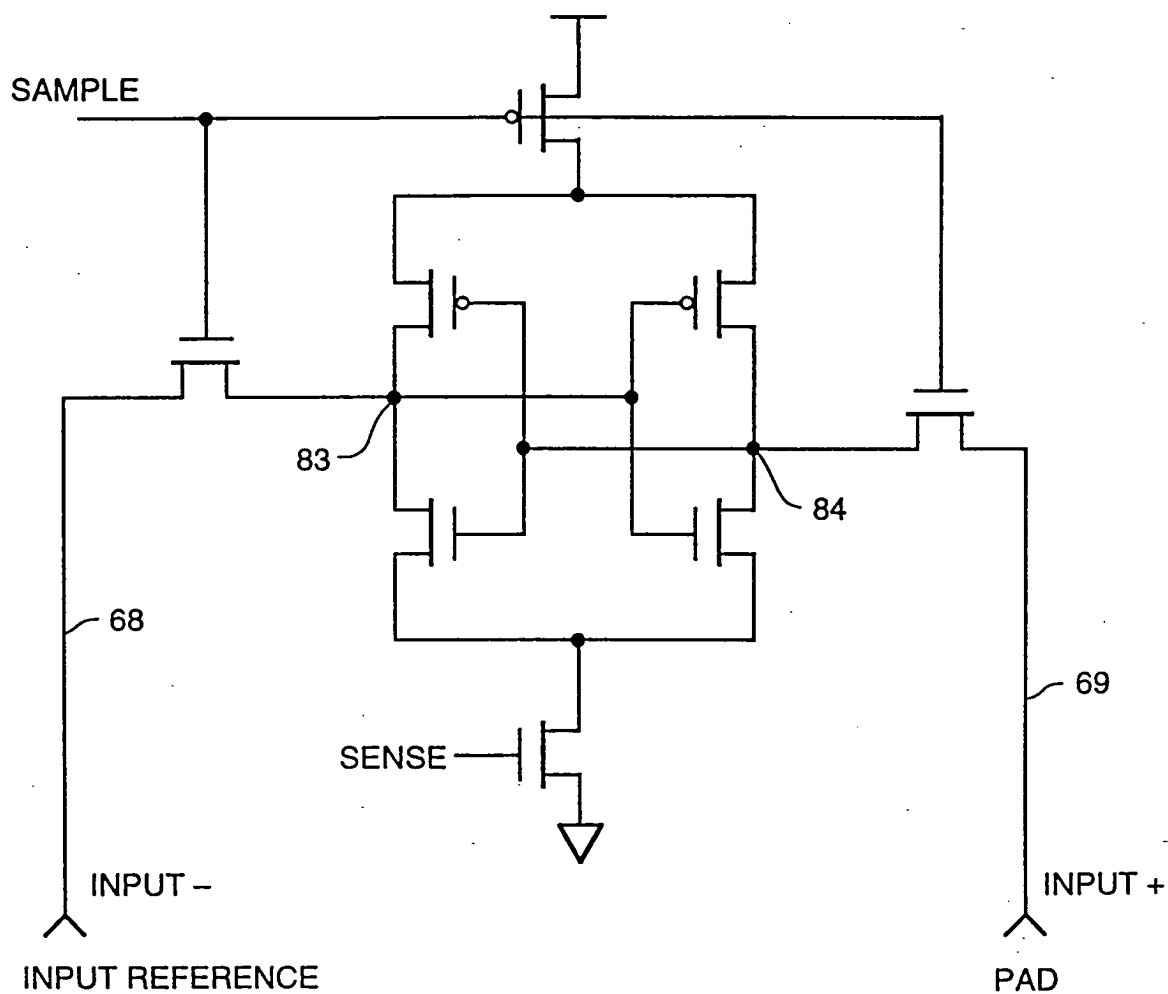


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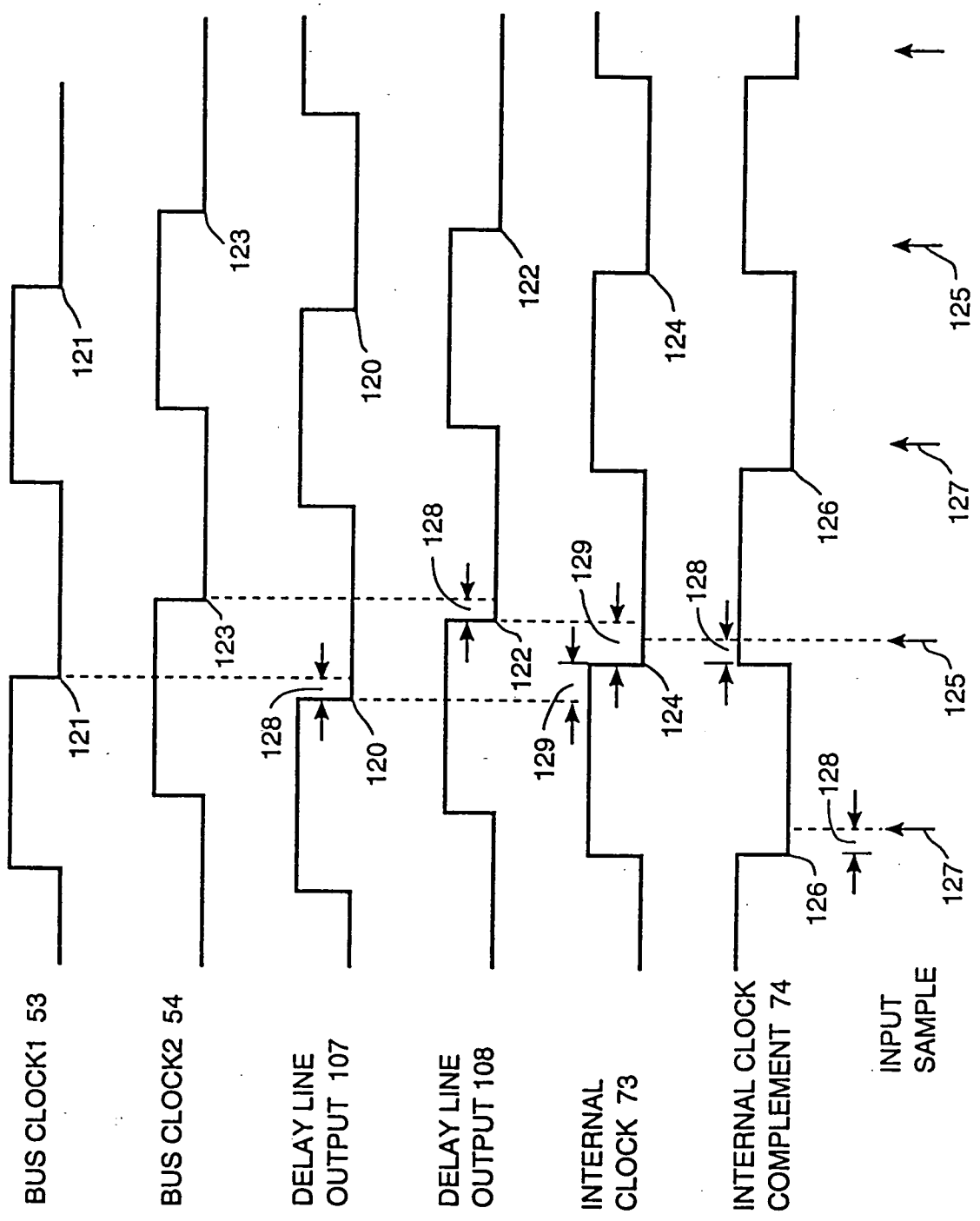


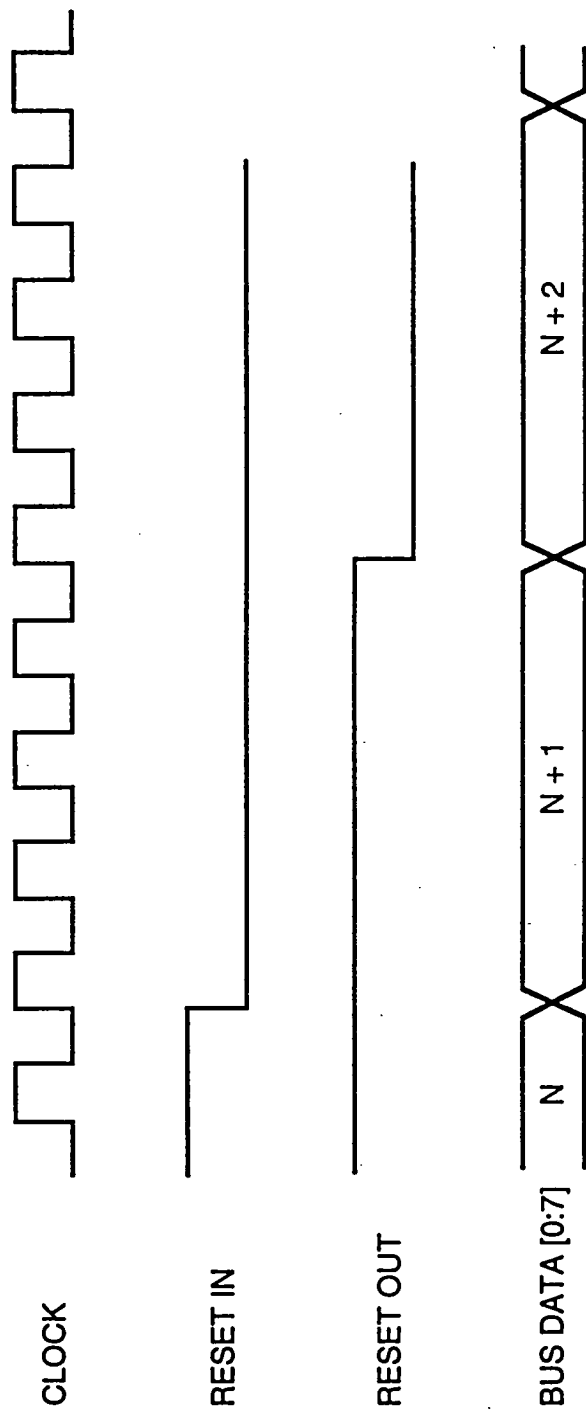
**FIG. 10**

**FIG. 11**









**FIG 14**

# FIG. 15

3 1 8 7 2

